

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application.

**LISTING OF CLAIMS:**

1. (Previously presented) A wireless receiver for receiving signals from a transmitter, the transmitter comprising a plurality of transmit antennas for transmitting the signals which comprise respective streams of independent symbols and wherein interference occurs between the respective streams, the receiver comprising:

a plurality of receive antennas for receiving the signals as influenced by a channel effect between the receiver and the transmitter;

circuitry for multiplying the signals with a conjugate transpose of an estimate of the channel effect and with a conjugate transpose of a linear basis transformation matrix;

circuitry for selecting the linear basis transformation matrix from a finite set of linear basis transformation matrices; and

circuitry for removing the interference between the respective streams.

2. (Original) The receiver of claim 1 and further comprising circuitry for determining the estimate of the channel effect, and wherein the circuitry for selecting selects the linear basis transformation matrix in response to the estimate of the channel effect.

3. (Original) The receiver of claim 1 wherein the circuitry for selecting comprises: circuitry for determining a signal-to-interference-noise ratio corresponding to each matrix in the finite set and to each of the streams; and

circuitry for selecting the linear basis transformation matrix as corresponding to an optimum determined signal-to-interference-noise ratios.

4. (Original) The receiver of claim 3 wherein each signal-to-interference-noise ratio is determined in response to the estimate of the channel effect.

5. (Original) The receiver of claim 3 wherein the circuitry for removing the interference between the respective streams is selected from a group consisting of circuitry for zero forcing and circuitry for determining a minimum mean square error.

6. (Original) The receiver of claim 5 wherein the circuitry for removing the interference between the respective streams is selected from a group consisting of iterative and linear.

7. (Original) The receiver of claim 3 wherein the circuitry for removing the interference between the respective streams comprises maximum likelihood detection circuitry.

8. (Original) The receiver of claim 3 wherein the circuitry for selecting comprises circuitry for identifying a minimum of the signal-to-interference-noise ratios corresponding to each matrix in the finite set, and wherein the circuitry for selecting selects the linear basis transformation matrix as corresponding to a maximum of the identified minimums.

9. (Original) The receiver of claim 3 wherein the circuitry for selecting comprises circuitry for minimizing a mean square error of the signal-to-interference-noise ratios corresponding to each matrix in the finite set.

10. (Original) The receiver of claim 1 and further comprising circuitry for communicating an identification of the linear basis transformation matrix to the transmitter via a feedback channel.

11. (Original) The receiver of claim 10:  
 wherein the finite set consists of a number  $N$  matrices; and  
 wherein the circuitry for communicating is operable to communicate the linear basis transformation matrix to the transmitter via a feedback channel by transmitting a number of bits equal to  $\log_2(N)$  bits to the transmitter.

12. (Original) The receiver of claim 11 wherein  $N$  is less than or equal to 16.

13. (Original) The receiver of claim 1 wherein each matrix in the finite set of linear basis transformation matrices is operable for performing a rotation by the transmitter of the symbols.

14. (Original) The receiver of claim 1 wherein each matrix in the finite set of linear basis transformation matrices is operable for performing a rotation and phase change by the transmitter of the symbols.

15. (Original) The receiver of claim 1 wherein each matrix in the finite set of linear basis transformation matrices comprises a form of  $\mathbf{V}^{(n)} = \begin{bmatrix} \cos \theta_n & -\sin \theta_n \\ \sin \theta_n & \cos \theta_n \end{bmatrix}$ .

16. (Original) The receiver of claim 1 wherein each matrix in the finite set of linear basis transformation matrices comprises a form of  $\mathbf{V}^{(n)} = \begin{bmatrix} \cos \theta_n e^{j\phi_n} & -\sin \theta_n e^{j\phi_n} \\ \sin \theta_n & \cos \theta_n \end{bmatrix}$ .

17. (Original) The receiver of claim 1 wherein each matrix in the finite set of linear basis transformation matrices comprises a permutation of a first matrix.

18. (Original) The receiver of claim 17 wherein the first matrix comprises an identity matrix.

19. (Original) The receiver of claim 1 wherein the circuitry for multiplying comprises space time block coded decoding circuitry.

20. (Original) The receiver of claim 1 wherein the circuitry for selecting comprises: circuitry for determining a signal-to-noise ratio corresponding to each matrix in the finite set and to each of the streams; and circuitry for selecting the linear basis transformation matrix as corresponding to an optimum determined signal-to-noise ratio.

21. (Original) The receiver of claim 1 and further comprising circuitry for determining the estimate of the channel effect in response to pilot symbols received from the transmitter.

22. (Original) The receiver of claim 1 wherein the circuitry for removing the interference between the respective streams is selected from a group consisting of circuitry for zero forcing, circuitry for determining a minimum mean square error, and circuitry for determining a maximum likelihood.

23. (Original) The receiver of claim 1 wherein the signals comprise CDMA signals and further comprising circuitry for despreading the CDMA signals.

24. (Original) The receiver of claim 1 wherein the signals comprise TDMA signals.

25. (Original) The receiver of claim 1 wherein the symbols are selected from a group consisting of quadrature phase shift keying symbols, binary phase shift keying symbols, and quadrature amplitude modulation symbols.

26. (Original) The receiver of claim 1 wherein the circuitry for multiplying the signals times a conjugate transpose of an estimate of the channel effect comprises a space time block coded transmit antenna diversity decoder.

27. (Original) The receiver of claim 1 wherein the plurality of transmit antennas and the plurality of receive antennas are a same number of antennas.

28. (Original) The receiver of claim 27 wherein the same number equals two.

29. (Original) The receiver of claim 1 wherein the plurality of transmit antennas are less in number than the plurality of receive antennas.

30. (Original) The receiver of claim 1 and further comprising:  
a demodulator coupled to receive an output from the circuitry for multiplying;  
a deinterleaver coupled to receive an output of the demodulator; and  
a decoder coupled to receive an output of the deinterleaver.

31. (Currently amended) A wireless communication system, comprising:  
a transmitter comprising a plurality of transmit antennas for transmitting signals which comprise respective streams of independent symbols and wherein interference occurs between the respective streams; and  
a receiver for receiving signals from the transmitter, ~~the transmitter comprising a plurality of transmit antennas for transmitting the signals which comprise respective streams~~

~~of independent symbols and wherein interference occurs between the respective streams, the receiver comprising:~~

- a plurality of receive antennas for receiving the signals as influenced by a channel effect between the receiver and the transmitter;
- circuitry for multiplying the signals with a conjugate transpose of an estimate of the channel effect and with a conjugate transpose of a linear basis transformation matrix;
- circuitry for selecting the linear basis transformation matrix from a finite set of linear basis transformation matrices; and
- circuitry for removing the interference between the respective streams.

32. (Original) The system of claim 31 wherein the receiver further comprises circuitry for determining the estimate of the channel effect, and wherein the circuitry for selecting selects the linear basis transformation matrix in response to the estimate of the channel effect.

33. (Original) The system of claim 31 wherein the receiver further comprises:  
circuitry for determining a signal-to-interference-noise ratio corresponding to each matrix in the finite set and to each of the streams; and  
circuitry for selecting the linear basis transformation matrix as corresponding to an optimum determined signal-to-interference-noise ratios.

34. (Original) The system of claim 31 wherein each matrix in the finite set of linear basis transformation matrices is operable for performing a rotation by the transmitter of the symbols.

35. (Original) The system of claim 31 wherein each matrix in the finite set of linear basis transformation matrices is operable for performing a rotation and phase change by the transmitter of the symbols.

36. (Original) The receiver of claim 31 wherein each matrix in the finite set of linear basis transformation matrices comprises a permutation of a first matrix.

37. (Original) The system of claim 31 wherein the receiver further comprises circuitry for communicating an identification of the linear basis transformation matrix to the transmitter via a feedback channel.

38. (Original) The system of claim 37 wherein the transmitter further comprises circuitry for multiplying the respective streams of independent symbols times the identified linear basis transformation matrix.

39. (Previously presented) A method of operating a wireless receiver, comprising:  
receiving signals at a plurality of receive antennas and transmitted from a transmitter, the transmitter comprising a plurality of transmit antennas for transmitting the signals which comprise respective streams of independent symbols and wherein interference occurs between the respective streams and wherein the received signals are influenced by a channel effect between the receiver and the transmitter;

    multiplying the signals with a conjugate transpose of an estimate of the channel effect and with a conjugate transpose of a linear basis transformation matrix;

    selecting the linear basis transformation matrix from a finite set of linear basis transformation matrices; and

    removing the interference between the respective streams.

40. (Original) The method of claim 39 and further comprising determining the estimate of the channel effect, and wherein the step of selecting selects the linear basis transformation matrix in response to the estimate of the channel effect.

41. (Original) The method of claim 39 wherein the step of selecting comprises:  
determining a signal-to-interference-noise ratio corresponding to each matrix in the finite set and to each of the streams; and  
selecting the linear basis transformation matrix as corresponding to an optimum determined signal-to-interference-noise ratios.

42. (Previously presented) A wireless transmitter for transmitting signals to a receiver, comprising:  
a plurality of transmit antennas for transmitting the signals, the signals comprising a plurality of independent streams of data symbols;  
circuitry for selecting a linear basis transformation matrix from one of at least two matrices in response to a communication received by the transmitter from the receiver via a feedback channel; and  
circuitry for multiplying the data symbols with the linear basis transformation matrix, wherein the signals are responsive to the multiplication with the linear basis transformation matrix.

43. (Original) The wireless transmitter of claim 42 wherein the receiver comprises:  
circuitry for selecting the linear basis transformation matrix; and  
circuitry for providing the communication to the transmitter via the feedback channel.

44. (Original) The wireless transmitter of claim 43 wherein the circuitry for selecting the linear basis transformation matrix selects from a finite set of linear basis transformation matrices.

45. (Original) The wireless transmitter of claim 44:  
wherein the finite set consists of a number  $N$  matrices; and



wherein the communication consists of a number of bits equal to  $\log_2(N)$  bits to the transmitter.

46. (Original) The wireless transmitter of claim 45 wherein  $N$  is less than or equal to 16.

47. (Original) The wireless transmitter of claim 42 wherein the linear basis transformation matrix is operable for performing a rotation by the transmitter of the symbols.

48. (Original) The wireless transmitter of claim 42 wherein the linear basis transformation matrix is operable for performing a rotation and phase change of the symbols.

49. (Original) The wireless transmitter of claim 42 wherein the linear basis transformation matrix comprises a form of  $\mathbf{V}^{(n)} = \begin{bmatrix} \cos \theta_n & -\sin \theta_n \\ \sin \theta_n & \cos \theta_n \end{bmatrix}$ .

50. (Previously presented) A wireless transmitter for transmitting signals to a receiver, comprising:

a plurality of transmit antennas for transmitting the signals, the signals comprising respective streams of independent symbols and wherein interference occurs between the respective streams;

circuitry for multiplying symbols with a linear basis transformation matrix, wherein the signals are responsive to the multiplication with a linear basis transformation matrix

comprising a form of  $\mathbf{V}^{(n)} = \begin{bmatrix} \cos \theta_n e^{j\phi_n} & -\sin \theta_n e^{j\phi_n} \\ \sin \theta_n & \cos \theta_n \end{bmatrix}$ ; and

circuitry for selecting the linear basis transformation matrix in response to a communication received by the transmitter from the receiver via a feedback channel .

51. (Previously presented) A wireless transmitter for transmitting signals to a receiver, comprising:

a plurality of transmit antennas for transmitting the signals, the signals comprising respective streams of independent symbols and wherein interference occurs between the respective streams;

circuitry for multiplying symbols with a linear basis transformation matrix, wherein the signals are responsive to the multiplication with a linear basis transformation matrix comprising a permutation of a first matrix; and

circuitry for selecting the linear basis transformation matrix in response to a communication received by the transmitter from the receiver via a feedback channel.

52. (Original) The wireless transmitter of claim 51 wherein the first matrix comprises an identity matrix.

53. (Original) The wireless transmitter of claim 42 and further comprising circuitry for space time block coded encoding the symbols.

54. (Original) The wireless transmitter of claim 42 wherein the signals comprise CDMA signals and further comprising circuitry for spreading the CDMA signals.

55. (Original) The wireless transmitter of claim 42 wherein the signals comprise TDMA signals.

56. (Original) The wireless transmitter of claim 42 wherein the symbols are selected from a group consisting of quadrature phase shift keying symbols, binary phase shift keying symbols, and quadrature amplitude modulation symbols.

57. (Previously presented) The wireless transmitter of claim 42:  
wherein the receiver comprises a plurality of receive antennas.

58. (Previously presented) The wireless transmitter of claim 57 wherein the plurality of transmit antennas and the plurality of receive antennas are a same number of antennas.

59. (Original) The wireless transmitter of claim 57 wherein the plurality of transmit antennas are less in number than the plurality of receive antennas.

60. (Currently amended) A method of processing signals in a communication circuit, comprising ~~the steps of~~:  
receiving a plurality of signals on a plurality of receive antennas from a plurality of transmit antennas coupled to a transmitter;  
producing a channel estimate in response to a predetermined signal of the plurality of signals;  
selecting a matrix from a finite set of matrices in response to one of the channel estimate and an interference cancellation technique; and  
multiplying the plurality of signals by the channel estimate and the matrix.

61. (Currently amended) A method as in claim 60, further comprising ~~the step of~~ despreading the plurality of signals in response to a code.

62. (Currently amended) A method as in claim 60, further comprising ~~the step of~~ removing interference from the plurality of signals.

63. (Currently amended) A method as in claim 60, further comprising ~~the steps in any order of~~:

identifying the selected matrix to a remote receiver; and

calculating a product of the channel estimate and the selected matrix prior to the step of multiplying.

64. (Currently amended) A method as in claim 60, further comprising ~~the steps in any order of~~:

converting a group of the plurality of signals to a serial signal;

demodulating the serial signals;

deinterleaving the serial signals; and

decoding the serial signals.

65. (Previously presented) A method as in claim 60, wherein the predetermined signal comprises at least one pilot symbol.

66. (Previously presented) A method as in claim 60, wherein the matrix is a linear basis transformation matrix.

67. (Currently amended) A method as in claim 60, further comprising ~~the step of~~ receiving the plurality of signals from a plurality of remote transmit antennas, wherein the plurality of signals are encoded differently for each respective antenna of the plurality of transmit antennas.

68. (Currently amended) A method of processing signals in a communication circuit, ~~comprising the steps of:~~  
receiving a plurality of signals;  
modulating the plurality of signals;  
selecting a matrix from one of at least two matrices in response to a signal from a remote transmitter;  
multiplying the modulated signals by the matrix; and  
transmitting the multiplied modulated signals from a plurality of transmit antennas.

69. (Previously presented) A method as in claim 68, further ~~comprising the step of~~ spreading the plurality of signals in response to a code.

70. (Cancelled)

71. (Currently amended) A method as in claim 68, further ~~comprising the steps in any order of:~~  
encoding the plurality of signals;  
interleaving the plurality signals; and  
converting the plurality of signals to serial signals.

72. (Currently amended) A method of processing signals in a communication circuit, ~~comprising the steps of:~~  
receiving a plurality of signals;  
modulating the plurality of signals;  
selecting a matrix from a finite set of matrices in response to a signal from a remote transmitter;  
multiplying the plurality of signals by the matrix; and

transmitting the plurality of signals from a plurality of transmit antennas, including transmitting a predetermined signal to a remote receiver on a channel different from a channel of the plurality of signals.

73. (Previously presented) A method as in claim 68, wherein the matrix is a linear basis transformation matrix.

74. (Currently amended) A method as in claim 68, further comprising ~~the step of~~ encoding the plurality of signals, wherein the plurality of signals are encoded differently for each respective antenna of the plurality of transmit antennas.